Research Associate in Open-Source Design Tools for Chip Design Verification

Hochschule München is among the largest German universities of applied sciences, located at the heart of Munich, Germany. The working group "AEMY" focuses on safe, secure and smart systems. Our main working areas include RISC-V processor design, WebAssembly runtime development and open source chip design tools.

We offer positions in our team for current and upcoming projects in the area of open source chip design tools.

Your tasks include:

- Contributions to open source chip design tools, in particular Verilator, CIRCT, Yosys
- Analysis of productivity gaps of verification tools for chip design
- Design and implementation of methods for verification of safety aspects

You are the excellent candidate, if you fullfill the following requirements

- Completed Master's degree in Computer Science, Electrical and Information Engineering, or comparable fields of study
- Basic understanding of digital design, ideally with experience in verification
- Solid understanding of modern workflows of open source software

Apply now!

https://stellen.hm.edu/jobposting/c5c6ba6069a9fea401ed9a844cd20a3ad556a0aa0

If you are interested, please get in touch:
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