



Research Associate in Open Source Tools for formal verification of digital circuits

Hochschule München is among the largest German universities of applied sciences, located at the heart of Munich, Germany. The working group "AEMY" focuses on safe, secure and smart systems. Our main working areas include RISC-V processor design, WebAssembly runtime development and open source chip design tools.

We offer positions in our team for current and upcoming projects in the area of open source chip design tools.

Your tasks include:

- Exploration of data models for incremental formal verification
- Design and Implementation of a database for formal verification
- Integration and extension of verification algorithms with the database
- Collaboration on the Yosys open-source suite

You are the excellent candidate, if you fullfill the following requirements

- Completed Master's degree in Computer Science, Electrical and Information Engineering, or comparable fields of study
- Basic understanding of digital circuit design, ideally with experience in verification
- Knowledge on formal methods in computer science
- Existing open source experience desirable

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If you are interested, please get in touch:

stefan.wallentowitz@hm.edu

Find more on our website:

<https://aemy.cs.hm.edu>

